Form PTO 1449		U.S. DEPARTMENT	OF COMMERCE	ATTY DOCKET NO.		SERIAL NO.				
(Modified)		PATENT AND TRAD	EMAKK OFFICE	250119US2DIV		New DIV Application				
				APPLICANT						
LIST OF	REFE	RENCES CITED BY API	PLICANT	Shingo YAMAGUCHI, et al.						
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U.S. PATENT DOCUMENTS										
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE			
m	ĀĀ	4,271,471,	06/02/81	Lotspiech	_	_				
	AB	5,819,110	10/06/98	Motoyama		_				
	AC	3,976,982	08-1976	Eiselen	_	1				
	AD	4,593,407	06-1986	Konishi, et al.	/					
	ΑE	5,381,163	01-1995	Yokoyama						
	AF	5,598,181	01-1997	Kermisch		_				
	AG	5,973,664	10-1999	Badger	_	-				
	АН	3,976,982	08-76	Eiselen		7				
	Al	5,204,916	04-93	Hamilton, et al.		-				
	AJ	6,094,510	07-00	Yaguchi, et al.						
	AK	5,012,434	04-1991	Zietłow	-					
	AL	5,133,025	07-1992							
	AM	5,973,721		Koyama et al.						
 	ÁN		10-1999	Bergmans et al.						
\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\	/	6,249,617	06/2001	Chen						
m	AO	6,360,029	03/2002	Moller		_				
			FOI	REIGN PATENT DOCUMENTS						
		DOCUMENT					TRANSI ATION			
		NUMBER	DATE	COUNTRY		TRANSLATION YES NO				
	AP				_					
	AQ									
	AR									
	AS									
	AT									
	AU									
	ΑV									
	AW									
OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, etc.)										
							FS 79-98			
50	AX	KAI HWANG ET AL., COMPUTER ARCHITECTURE AND PARALLEL PROCESSING, 1984, PAGES 79-98.								
6	ΔΥ	PRELIMINARY PRODUCT BRIEF OF THE V _{RC} 4373 INTERFACE CONTROLLER FOR THE V _R 4300 MICROPROCESSOR,								
1	Α,	JULY 1997, 2 PAGES.								
ja	AZ									
n	AZA	PRESS RELEASE OF NEC VRC4347 INTERFACE AND MEMORY CONTROLLER, JULY 1997, 2 PAGES. Additional References sheet(s) attached								
Examiner	Date Considered 3. 6-06						3.6-06			
*Examiner: In conformance	*Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.									
*Examiner: In	AY AZ AZA	BROCHURE ON NEC V _R 4300 MICROPROCESSOR, 1997, 1 PAGE. PRESS RELEASE OF NEC VRC4347 INTERFACE AND MEMORY CONTROLLER, JULY 1997, 2 PAGES. Date Considered 3 6 6 6 efference is considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in								

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(Modified)			250119US2DIV	New DIV Application			
			APPLICANT	1			
LIST OF	REFE	RENCES CITED BY APPLICANT	Shingo YAMAGUCHI, et al.				
			FILING DATE	GROUP			
	•		Herewith				
		OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages	s, etc.)			
h	AAA	"SPECIFICATIONS FOR SPLASH PCI 1280", FROM THE INTERNET PRINTED IN SEPTEMBER OF 1998, 2 PAGES.					
4	AAB	"SPECIFICATIONS FOR SPLACH PCI 640E", PRINTED FROM THE INTERNET IN SEPTEMBER OF 1998, 2 PAGES.					
Gu	AAC	"THE INDISPENSABLE PENTIUM BOOK", BY HANS-PETER MESSMER, PUBLISHED BY ADDISON-WESLEY, 1995, PP. 207-218.					
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